TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOS IV)

TPC8042

Lithium-Ion Battery Applications
Portable Equipment Applications
Notebook PC Applications

Small footprint due to a small and thin package

• Low drain-source ON-resistance: R_{DS} (ON) = 2.7 m Ω (typ.)

• High forward transfer admittance: $|Y_{fs}| = 42 \text{ S (typ.)}$

• Low leakage current: $IDSS = 10 \mu A (max) (VDS = 30 V)$

• Enhancement mode: $V_{th} = 1.3$ to 2.5 V ($V_{DS} = 10$ V, $I_{D} = 1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage (R	$R_{GS} = 20 \text{ k}\Omega$	V_{DGR}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	18	Α	
Drain current	Pulse (Note 1)	I _{DP}	72	A	
Drain power dissipati	on (t = 10 s) (Note 2a)	P_{D}	1.9	W	
Drain power dissipati	on (t = 10 s) (Note 2b)	P _D	1.0	W	
Single pulse avalanch	ne energy (Note 3)	E _{AS}	84	mJ	
Avalanche current		I _{AR}	18	Α	
Repetitive avalanche (energy Note 2a) (Note 4)	E _{AR}	0.044	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

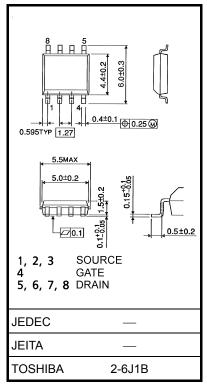
temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

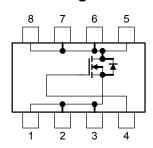
This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



Weight: 0.08 g (typ.)

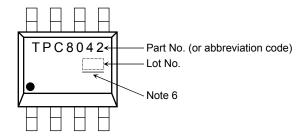
Circuit Configuration



Thermal Characteristics

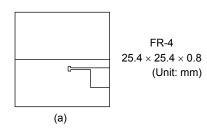
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

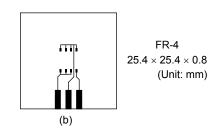
Marking (Note 5)



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)



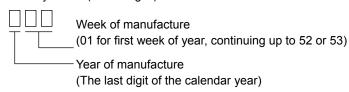


Note 3: $V_{DD} = 24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.2 mH, $I_{AR} = 18 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on the lower left of the marking indicates Pin 1.

* Weekly code: (Three digits)



Note 6: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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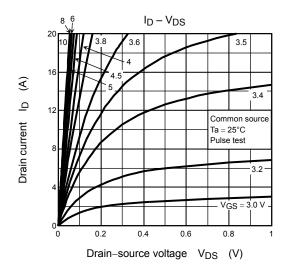
Electrical Characteristics (Ta = 25°C)

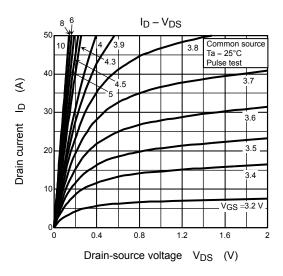
Ch	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rrent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cut-OFF cu	urrent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА
Drain-source breakdown voltage		V _{(BR) DSS}	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
Diam-source bre	akuowii voitage	V _{(BR) DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	10	10 — — 1.3 — 2.5		
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.3	_	2.5	V
Drain-source ON-resistance		D= - /	V _{GS} = 4.5 V, I _D = 9 A	_	4.2	6.5	- mΩ
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 9 A		2.7	3.4	
Forward transfer	ward transfer admittance $ Y_{fS} $ $V_{DS} = 10 \text{ V}, I_D = 9 \text{ A}$		V _{DS} = 10 V, I _D = 9 A	21	42	_	S
Input capacitance		C _{iss}			2900	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		460	_	
Output capacitance		C _{oss}			800	_	
Reverse transfer capacitance	Rise time	t _r	VDD ≈ 12 V NDD ≈ 12 V NDD ≈ 12 V NDD ≈ 12 V	_	18	_	- ns
	Turn-on time	t _{on}		_	32	_	
	Fall time	t _f		_	25	_	
	Turn-off time	t _{off}	Duty ≤ 1%, t _W = 10 μs	_	81	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 18 \text{ A}$	_	56	_	nC
Gate-source charge 1		Q _{gs1}		_	9	_	
Gate-drain ("miller") charge		Q _{gd}		_	17	_	

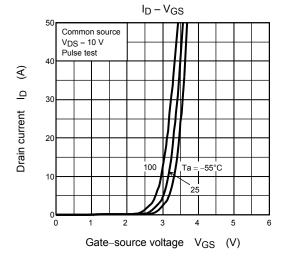
Source-Drain Ratings and Characteristics (Ta = 25°C)

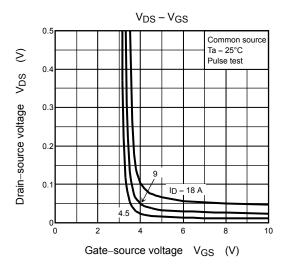
Characteri	stics		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	72	Α
Forward voltage (diode)			V_{DSF}	I _{DR} = 18 A, V _{GS} = 0 V	_	_	-1.2	V

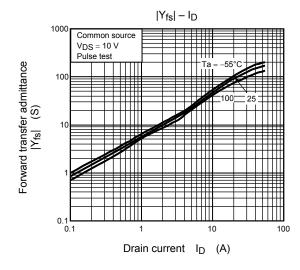
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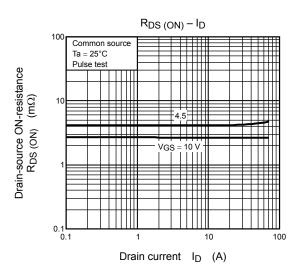


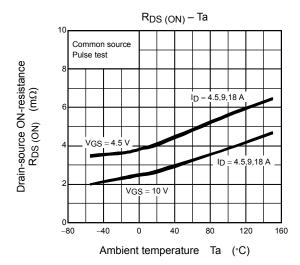


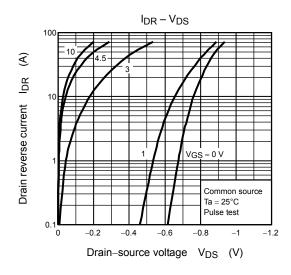


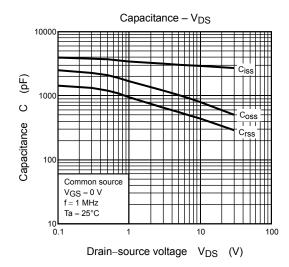


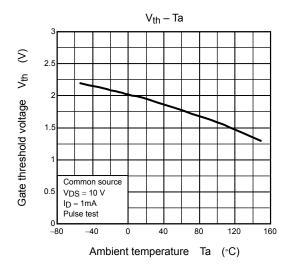


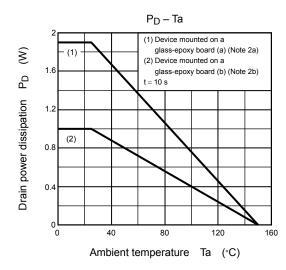


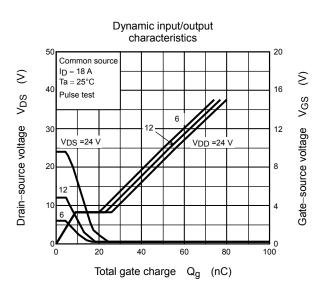


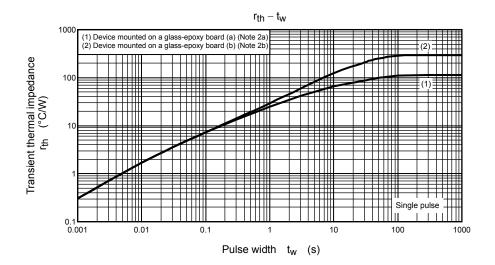


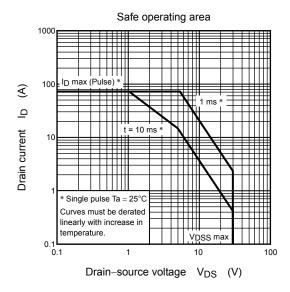












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